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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10043099	01/14/2002	438		2811	

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**\*\*CONTINUING DATA VERIFIED:**

**\*\* FOREIGN APPLICATIONS VERIFIED:**  
 JAPAN 2001-41097 02/19/2001  
 JAPAN 2001-8306 01-16/2001

<input type="checkbox"/> F.C. PUB <input type="checkbox"/> DO NOT PUBLISH Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners initials	<input type="checkbox"/> RESCIND	ATTORNEY DOCKET NO 500 41080.X00
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TITLE : Semiconductor device and its production process

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED		
Amount Due Date Paid		Assistant Examiner		Total Claims Print Claim for O.G
		Primary Examiner		DRAWING Sheets Drwg. Figs.Drwg. Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		PREPARED FOR ISSUE		Application Examiner
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